

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Schloesser, et al.

Docket No.: INF-135

**FAX RECEIVED**

Serial No.: 10/777,128

Art Unit: 2818

APR 07 2006

Filed: February 13, 2004

Examiner: Andy Huynh

**OFFICE OF PETITIONS**

For: Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells

Mail Stop DAC  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**PETITION FOR WITHDRAWAL FROM ISSUE UNDER 37 C.F.R. § 1.313(c)(2)****PETITION**

1. Applicants hereby petition for the withdrawal of this application from issue.

**PATENT ISSUE FEE**

2. The issue fee for this case was paid on January 16, 2006.

**REASON(S) FOR WITHDRAWAL REQUEST**

3. The reason for withdrawal from issue of this application is for consideration of an Information Disclosure Statement and Request for Continued Examination. 37 C.F.R. § 1.313(c)(2).

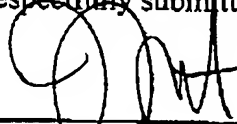
**PETITION FEE**

4. Petition Fees (37 C.F.R. § 1.17(h) (\$130.00)). The petition fee set forth in § 1.17(h), required by 35 C.F.R. § 1.313(a) is hereby authorized to be charged to Deposit Account No. 50-1065. Charge any additional fees required by this paper or credit any overpayment in the manner authorized above.

04/11/2006 CKHLOK 00000003 501065 10777128

01 FC:1464 130.00 DA

Respectfully submitted,

Ira S. Matsil  
Reg. No. 35,272Slater & Matsil, L.L.P.  
17950 Preston Rd., Suite 1000  
Dallas, TX 75252  
Tel: 972-732-1001  
Fax: 972-732-9218



17950 Preston Road | Suite 1000 | Dallas, TX 75252

FAX RECEIVED

APR 07 2006

OFFICE OF PETITIONS

(972) 732-1001

(972) 732-9218 FAX

www.slater-matsil.com

## FACSIMILE TRANSMITTAL SHEET

\*\*\* THE ACCOMPANYING MATERIAL MAY BE PRIVILEGED AND CONFIDENTIAL. IT IS INTENDED FOR THE SOLE USE OF THE NAMED RECIPIENT. PLEASE DELIVER PROMPTLY TO THE NAMED RECIPIENT ONLY. IF YOU HAVE RECEIVED THIS MATERIAL IN ERROR, PLEASE TELEPHONE THE SENDER AT (972) 732-1001 FOR INSTRUCTIONS. \*\*\*

TO: Commissioner for Patents  
Office of Petitions

FAX NO.: 571-273-0025

TEL NO.:

FROM: Ira S. Matsil

DATE: April 7, 2006

NO. OF PAGES: 18  
(including cover page)

Applicant: Schloesser, et al.

Docket No: INF-135

Serial No.: 10/777,128

Art Unit: 2818

Filed: February 13, 2004

Examiner: Andy Huynh

For: Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells

---

### Certificate of Transmission under 37 C.F.R. § 1.8

I hereby certify that the following correspondence is being facsimile transmitted to the Patent and Trademark Office on April 7, 2006.

Facsimile Cover Sheet with Certificate of Transmission (1 page)  
Petition for Withdrawal from Issue Under 37 C.F.R. § 1.313(c)(2) (1 original and 1 copy)  
Request for Continued Examination (RCE) Transmittal (2 duplicate pages)  
Information Disclosure Statement (1 page)  
Form PTO/SB08A (1 page), with 4 references cited  
Copy of one (1) cited reference (11 pages)

Signature

AnneMarie James  
Name of Person Signing the Certificate